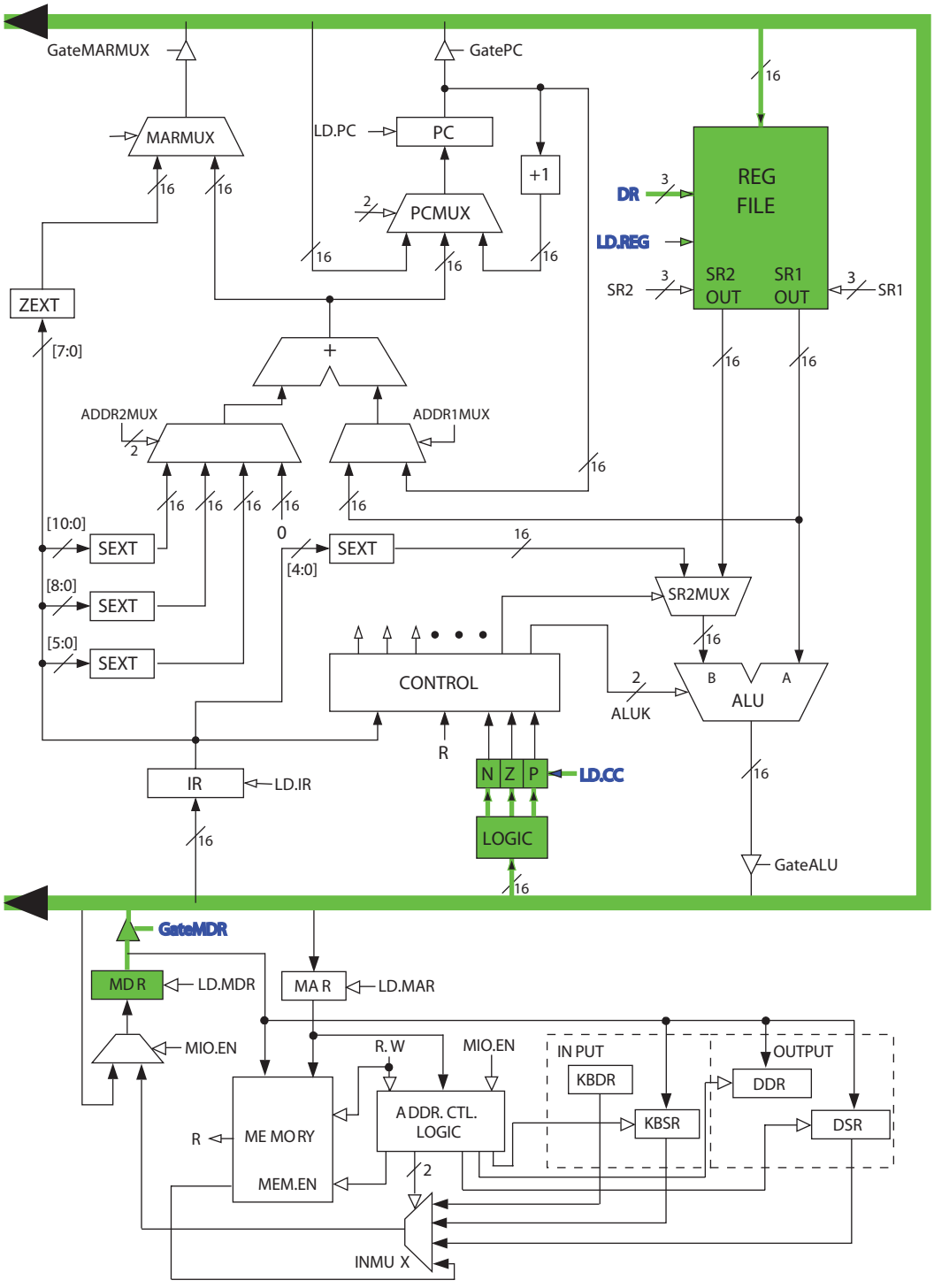

GateMARMUX
GatePC
MARMUX
LD.PC
PC
+1
PCMUX
REG FILE
DR
LD.REG
SR2
SR1
SR2 OUT
SR1 OUT
ZEXT
[7:0]
+
ADDR2MUX
ADDR1MUX
[10:0]
[8:0]
[5:0]
SEXT
SEXT
SEXT
[4:0]
SR2MUX
CONTROL
ALUK
ALU
B
A
R
N
Z
P
LD.CC
IR
LD.IR
LOGIC
GateALU
GateMDR
MDR
LD.MDR
MAR
LD.MAR
MIO.EN
R.W
MIO.EN
IN PUT
KBDR
KBSR
OUTPUT
DDR
DSR
R
MEMORY
MEM.EN
ADDR. CTL. LOGIC
INMUX