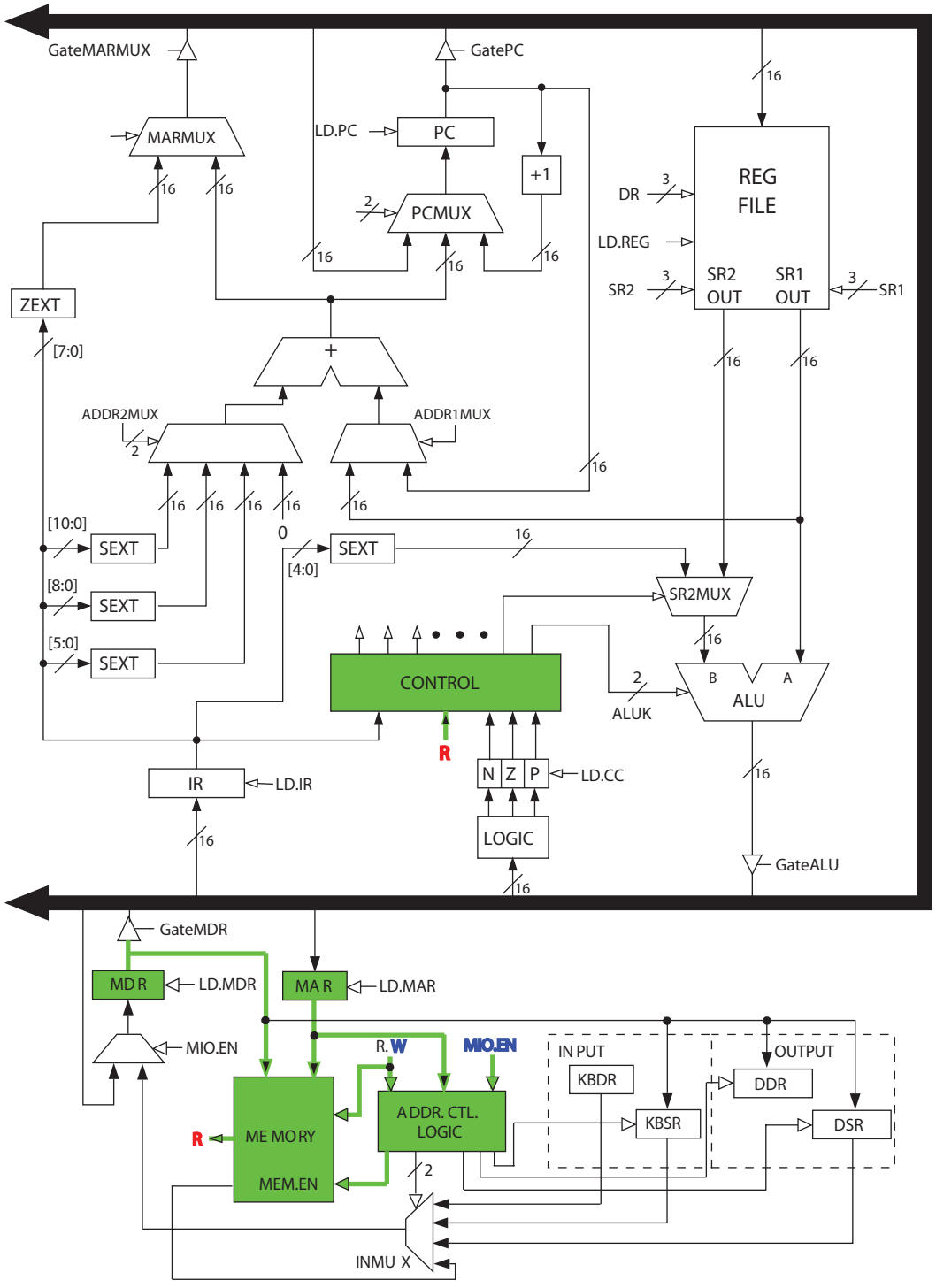
GateMARMUX
GatePC
MARMUX
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PC
+1
PCMUX
REG
FILE
DR
LD.REG
SR2
SR2
OUT
SR1
OUT
SR1
ZEXT
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+
ADDR2MUX
ADDR1MUX
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SEXT
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SEXT
0
SEXT
[4:0]
SR2MUX
CONTROL
ALUK
B
A
ALU
R
N
Z
P
LD.CC
IR
LD.IR
LOGIC
GateALU
GateMDR
MDR
LD.MDR
MAR
LD.MAR
MIO.EN
R.W
MIO.EN
IN PUT
KBDR
KBSR
OUTPUT
DDR
DSR
ADDR. CTL.
LOGIC
R
MEMORY
MEM.EN
INMUX